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By:

Date:

September 15, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Peter Haneder et al.
Applic. No. : 09/801,209
Filed : March 7, 2001
Title : Ferroelectric Transistor, Use Thereof In A
Memory Cell Configuration And Method Of
Producing The Ferroelectric Transistor
Examiner : Thao X. Le
Group Art Unit : 2814

A M E N D M E N T

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated September 8, 2003 kindly
amend the above-identified application as follows:

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